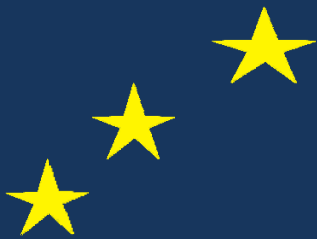


EUROTRAINING
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NEWS



The EuroTraining team consists of Technoconsult, Technical University of Denmark (DTU), Consorzio per la Ricerca e Educazione Permanente (COREP), Budapest University of Technology and Economics (BME), and LETI.

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EuroTraining provides a European Training Infrastructure facilitating the provision of high calibre training across Europe. The structure supports professional advancement training as well as academic training. Professional course providers get a central place for the presentation of their training offer while academics get a course material exchange service.

The EuroTraining Course Directory offers you up-to-date course information about available European microelectronics and microsystems courses. We provide a full search facility which allows you to find exactly the course you want.

A new launch of EuroTraining

Building on the key assets of the former project, the new EuroTraining project was launched on 1 November 2007. An important motivation for the new project is that universities are also facing stronger "time-to-market" requirements and thereby a faster curricula development cycle. This is very resource demanding and most universities cannot afford to do this within their ordinary budget. Therefore it would be welcomed to share courses, course material or modules with other universities provided that such courses and modules are creditable, i.e. holding high quality standards. Furthermore it would be advantageous if such courses also could be made available to industry in order to strengthen continuing professional training.

"universities are also facing stronger "time-to-market" requirements and thereby a faster curricula development cycle."

The action offers access to a comprehensive range of advanced training courses, course material and thematic SME employee training support. Special attention is devoted to stimulate the development of new courses supporting the CMOS technology targeting digital components and complex digital SoCs ("More Moore"); to master diversification targeting non-digital applications, heterogeneous integration in SoCs or SiPs ("More than Moore") and to prepare for the technology generation beyond the CMOS scaling limits ("beyond CMOS").

Key elements of the EuroTraining project are:

1. A continuing training programme to offer high calibre quality labelled training in all Europe. Two very important EuroTraining assets are the European Course Directory containing more than 400 updated continuing training courses and the accredited Quality Labelling service.
2. An action to support graduate schools in micro- and nanoelectronics with courses, workshops and training material.
3. All services offered are gathered in the European Training Infrastructure web site, promoting the training services and attracting participants.

Because of the enormous success of the electronic course directory COREP and DTU decided to maintain this service during the gap between the EU contracts. Therefore, the new project is ready to announce pan European courses right from the start.

Training roadmaps

In the new EuroTraining project (ICT-2007-211806) we are planning two roadmap activities on education and training. The first task, which is headed by prof. Zsolt Illyefalvi-Vitez from Budapest University of Technology and Economics, concerns industrial course requirements while the second task, headed by prof. Erik Bruun, Technical University of Denmark, concerns university course requirements. The intention is to establish the required industrial training needs as well as the course needs for graduate schools.

"we are planning to establish a so called nanoExpert Board"

We are currently drafting the workplan on these tasks which among other things will include contact to key training users, selected IP/NoE project leaders and study of US university curricula. The project wants to make use of the expertise build up in various research projects and in ENIAC's working group on Education and Training. To this end we are planning to establish a so called "nanoExpert" Board consisting of eight persons. The two training roadmaps will be presented during spring 2008.

EuroTraining actively encourages flexible learning. It offers basic and advanced training courses. See the course calendar on next page.

The following courses have been awarded the Quality Label by the European Commission's EuroTraining programme and are organised by professional European course providers. For more detailed information, including course contents, please refer to the EuroTraining Course Directory on our web site <http://www.eurotraining.net>. Here, you will also be able to submit course registrations on-line.

| <i>Date commencing</i> | <i>Title</i> | <i>Location</i> |
|------------------------|---|-------------------|
| 10 Dec 2007 | Allegro Design Reuse | Velizy, France |
| 10 Dec 2007 | VHDL Application Workshop | Munich, Germany |
| 10 Dec 2007 | Verilog Application Workshop | Velizy, France |
| 10 Dec 2007 | SpecMan Elite Verification Environment | Bracknell, UK |
| 10 Dec 2007 | IC Design for Automotive Environments | Livingston, UK |
| 10 Dec 2007 | Allegro PCB Editor | Kista, Sweden |
| 10 Dec 2007 | Allegro PCB Editor | Bracknell, UK |
| 10 Dec 2007 | SiP Digital Layout | Munich, Germany |
| 11 Dec 2007 | Labs-on-Chip Technologies: Basics and Applications | Vienna, Austria |
| 11 Dec 2007 | Allegro High Speed Constraint Management | Velizy, France |
| 16 Dec 2007 | SystemVerilog Assertions (SVA) | Velizy, France |
| 17 Dec 2007 | SpecMan Elite Verification Environment | Munich, Germany |
| 17 Dec 2007 | Introduction to Perl | Munich, Germany |
| 18 Dec 2007 | Allegro AMS Simulator Advanced Analysis | Munich, Germany |
| 14 Jan 2008 | Fundamental Analogue IC Design | Limerick, Ireland |
| 15 Jan 2008 | EMC Lab Techniques for Designers | Oxford, UK |
| 16 Jan 2008 | High Frequency Measurements | Oxford, UK |
| 18 Jan 2008 | Failure Analysis and Prevention in Electronic Circuits | Oxford, UK |
| 21 Jan 2008 | Practical RF/Microwave Design | Oxford, UK |
| 22 Jan 2008 | Digital Signal Processing | Oxford, UK |
| 25 Jan 2008 | Digital Signal Processing Implementation | Oxford, UK |
| 7 Feb 2008 | Successful RF PCB Design | Oxford, UK |
| 10 Mar 2008 | Effective Project Management for Engineers, Scientists & Staff in Hi-Tech Companies | Oxford, UK |
| 11 Mar 2008 | Successful Change Management for Engineers, Scientists & Staff in Hi-Tech Companies | Oxford, UK |